



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Reithinger, *et al.* Docket No.: 00P7685US
Serial No.: 09/596,129 Art Unit: 2814
Filed: June 16, 2000 Examiner: Phat X. Cao
For: Multiple Chip Semiconductor Arrangement having Electrical Components
in Separating Regions

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: January 19, 2004

I hereby certify that the below listed correspondence is being deposited with the United States Postal Service on the date indicated above as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Certificate of Mailing via First Class Mail (1 page)
Fee Transmittal (1 original and 1 copy = 2 pages)
Request for Continued Examination Transmittal (1 page)
Amendment (17 pages)
Information Disclosure Statement (1 page)
Form PTO/SB/08A (1 page) w/5 cited references and copy of PCT Search Report
Two (2) Return Postcards

Respectfully submitted,

Julie Russell
Legal Assistant

Slater & Matsil, L.L.P.
17950 Preston Rd., Suite 1000
Dallas, TX 75252
Tel: 972-732-1001
Fax: 972-732-9218